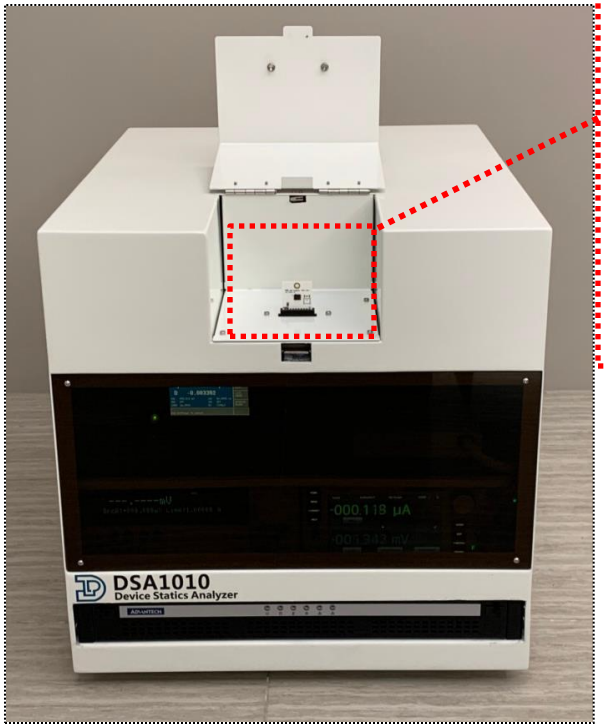
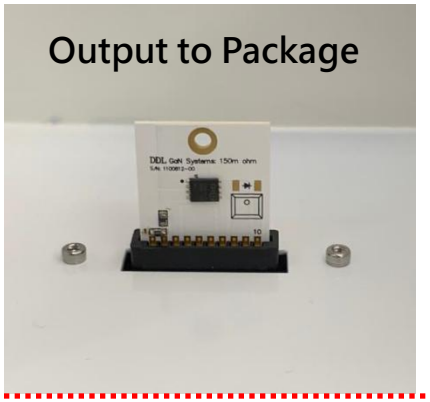
DSA1010半導體靜態參數分析儀(Device Statics Analyzer)

The DSA1010 Device Statics Analyzer can help developer to improve components, and it could also correctly evaluate the power loss and aging rate of components. Different from other power device analyzers, DSA1010 has a friendly operating interface, which can achieve intuitive use without programming, can measure and automatically adjust IV & CV at the same time, automatically switch between SMU and CMU modes, and has flexibility Features of the interface of the DUT.

主要技術規格
DC : 1000V/10mA, 40V/3A
Pulse : 20V/10A, 35V/5A
Friendly user interface - Transistor mode or Diode mode - Testing Plan for series tests
Flexible DUT connection - Package-Level - Wafer-Level
I-V & C-V automation - SMU + CMU auto switch - HV Bias-Tee integrated
DC & Pulse automation - DC SMU + Pulse SMU auto switch



Output to Package



Output to Wafer

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Friendly user interface

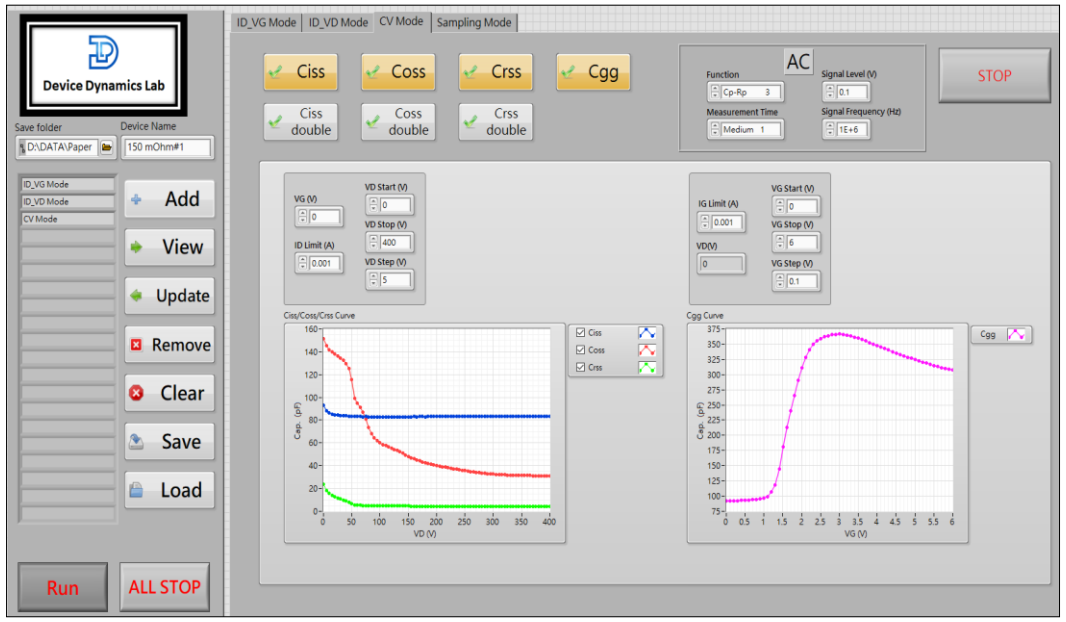

Figure of Merit (FOM) :

- Normalized to Rdsonfor size independent
- Rdson*Die size = Rspfor wafer cost
- Rdson*Qossfor reverse (diode) switching loss
- Rdson*Qgdfor trun-on speed (switching loss)
- Rdson*Eossfor turn-off switching loss

FOM	Transphorm	GaN Systems	Infineon	Unit
Rdson	116	123	136	mΩ
Qoss	36	18.9	15.7	nC
Qgd	1.69	0.55	0.62	nC
Eoss	3.84	2.22	2.56	μJ
Ron*Qoss	4176	2325	2135	mΩ*nC
Ron*Qgd	196.0	67.7	84.3	mΩ*nC
Ron*Eoss	445.4	273.1	348.2	mΩ*μJ